

### General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
-100V	200mΩ	-4A

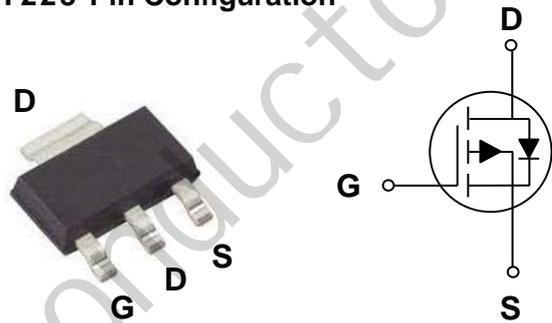
### Applications

- Motor Drive
- Power Tools
- LED Lighting

### Features

- -100V, -4A,  $R_{DS(ON)} = 200m\Omega$  @  $V_{GS} = -10V$
- Improved  $dv/dt$  capability
- Fast switching
- Green Device Available

### SOT223 Pin Configuration



### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ\text{C}$ )	-4	A
	Drain Current – Continuous ( $T_A=70^\circ\text{C}$ )	-1.6	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	-8	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	24	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	22	A
$P_D$	Power Dissipation ( $T_A=25^\circ\text{C}$ )	1.78	W
	Power Dissipation – Derate above $25^\circ\text{C}$	0.014	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	70	$^\circ\text{C}/\text{W}$

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-100	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-100V, V <sub>GS</sub> =0V, T <sub>J</sub> =25 °C	---	---	-1	uA
		V <sub>DS</sub> =-80V, V <sub>GS</sub> =0V, T <sub>J</sub> =125 °C	---	---	-10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA

**On Characteristics**

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-2A	---	165	200	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1.5A	---	180	230	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-1.2	-1.6	-2.2	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>S</sub> =-1.5A	---	6.5	---	S

**Dynamic and switching Characteristics**

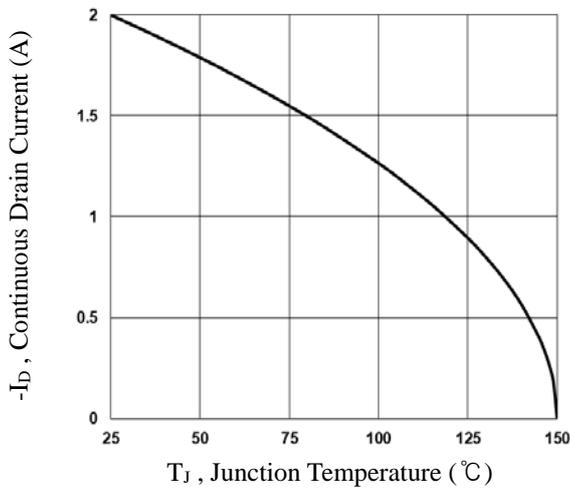
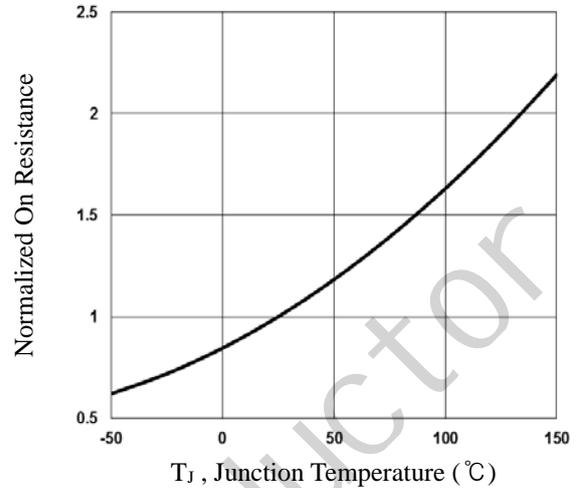
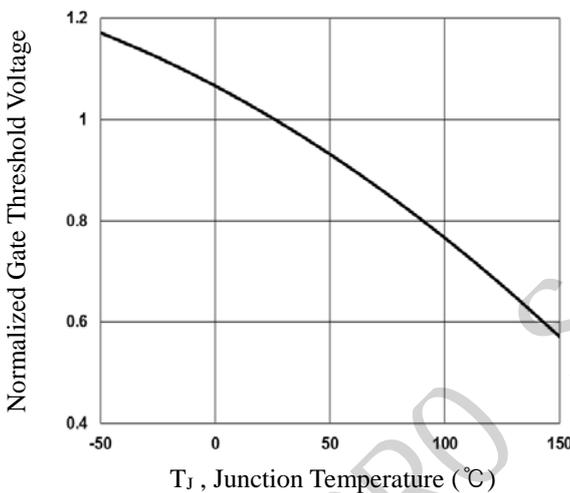
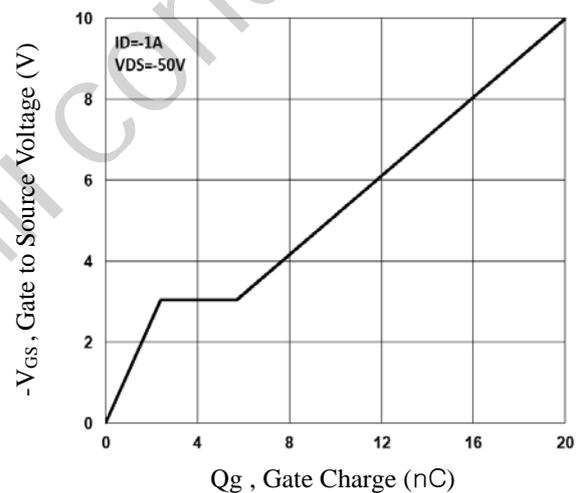
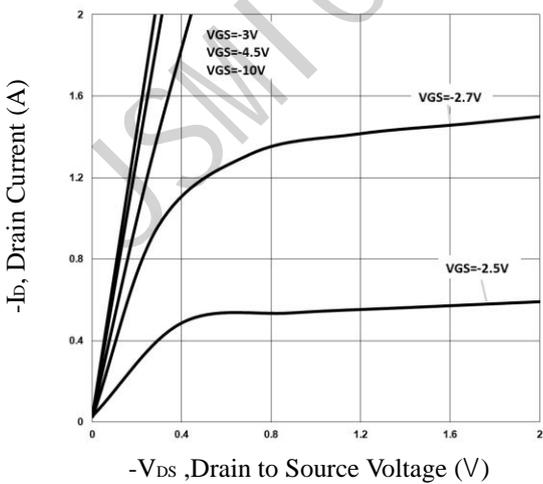
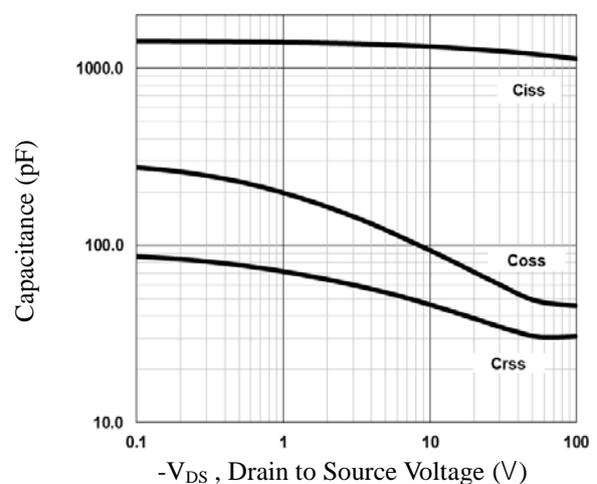
Q <sub>g</sub>	Total Gate Charge <sup>2, 3</sup>	V <sub>DS</sub> =-50V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-1A	---	20	30	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>2, 3</sup>		---	2.4	5	
Q <sub>gd</sub>	Gate-Drain Charge <sup>2, 3</sup>		---	3.3	7	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2, 3</sup>	V <sub>DD</sub> =-50V, V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω I <sub>D</sub> =-1A	---	18	27	ns
T <sub>r</sub>	Rise Time <sup>2, 3</sup>		---	8	12	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>2, 3</sup>		---	100	150	
T <sub>f</sub>	Fall Time <sup>2, 3</sup>		---	30	45	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-50V, V <sub>GS</sub> =0V, F=1MHz	---	1280	2000	pF
C <sub>oss</sub>	Output Capacitance		---	55	100	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	30	60	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	---	16	---	Ω

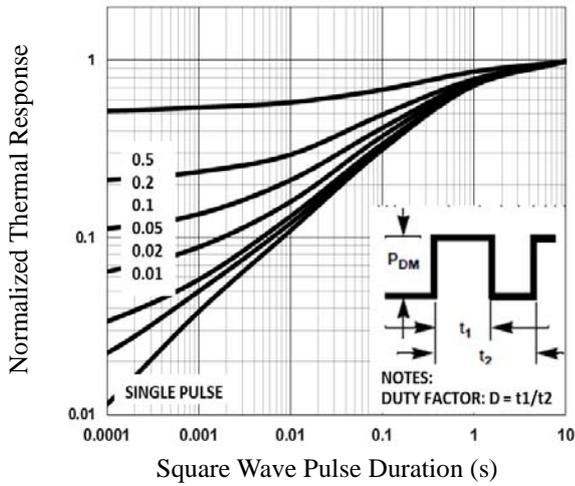
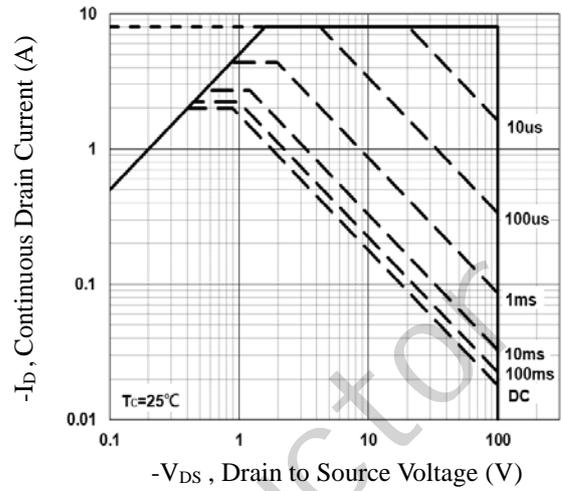
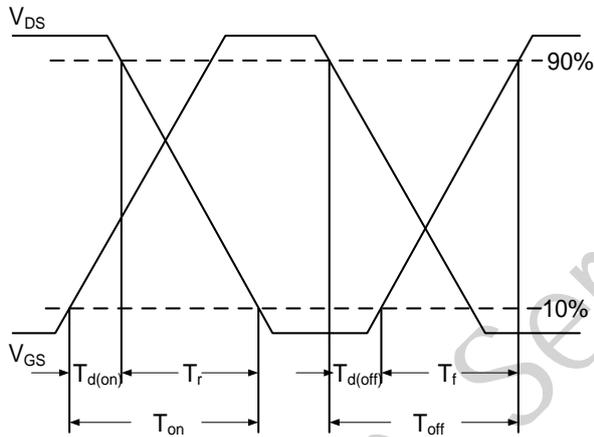
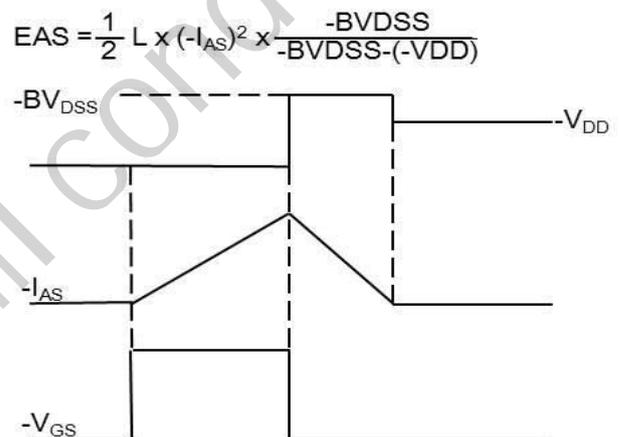
**Drain - Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	-4	A
I <sub>SM</sub>	Pulsed Source Current		---	---	-4	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25 °C	---	---	-1	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>R</sub> =-100V, I <sub>S</sub> =-1A	---	35	---	ns
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/μs, T <sub>J</sub> =25 °C	---	30	---	nC

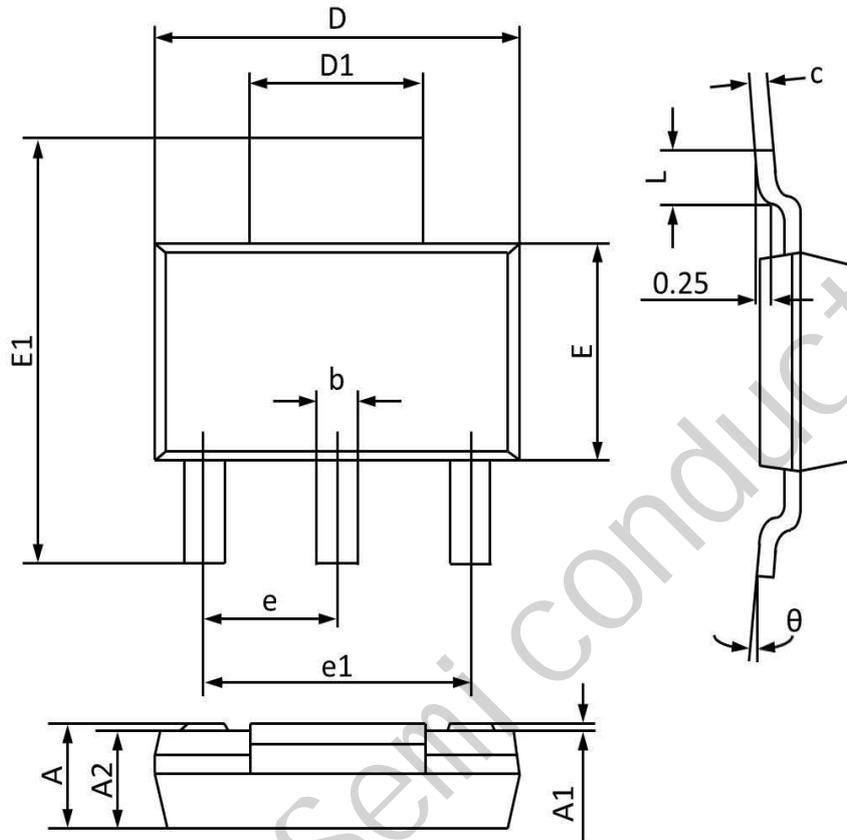
**Note :**

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=22A., Starting T<sub>J</sub>=25 °C
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_J$** 

**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$** 

**Fig.3 Normalized  $V_{th}$  vs.  $T_J$** 

**Fig.4 Gate Charge Waveform**

**Fig.5 Typical Output Characteristics**

**Fig.6 Capacitance Characteristics**

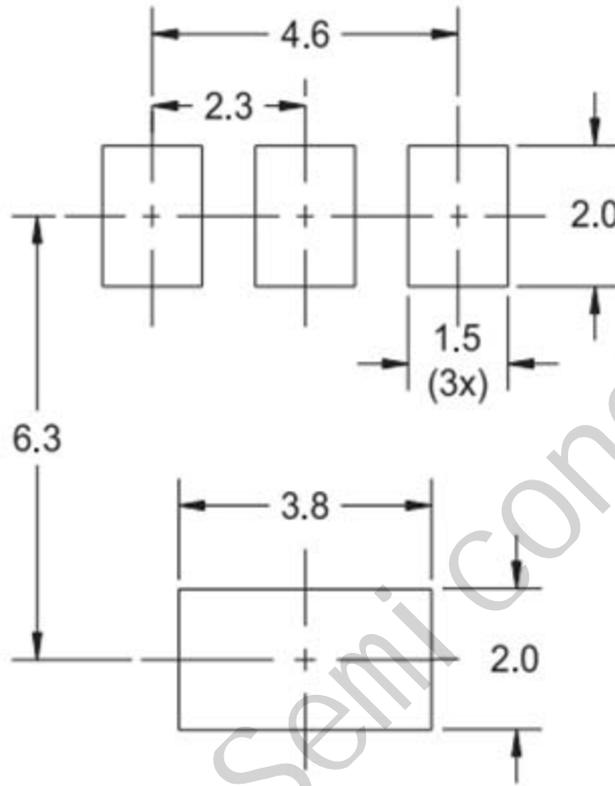

**Fig. 7 Normalized Transient Impedance**

**Fig. 8 Maximum Safe Operation Area**

**Fig. 9 Switching Time Waveform**

**Fig. 10 EAS Waveform**

## SOT223 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.800	1.500	0.071	0.060
A1	0.120	0.000	0.005	0.000
A2	1.750	1.450	0.069	0.057
b	0.820	0.600	0.032	0.024
c	0.350	0.200	0.014	0.008
D	6.700	6.200	0.264	0.244
D1	3.100	2.900	0.122	0.114
E	3.700	3.300	0.146	0.130
E1	7.300	6.700	0.287	0.264
e	2.30(BSC)		0.091(BSC)	
e1	4.700	4.400	0.185	0.173
L	1.150	0.900	0.045	0.035
θ	10°	0°	10°	0°

### SOT223 RECOMMENDED LAND PATTERN



unit : mm